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CLAIMS

1. A method of automated enumeration of one or more devices comprising the steps of:

(A) generating an enumeration of a plurality of fuses;

5 and

(B) compiling data for each one of said plurality of fuses, wherein said data comprises (i) one or more schematic path data, (ii) one or more simulation path data and/or (iii) one or more physical location data.

2. The method according to claim 1, wherein said one or more simulation path data comprises verilog simulation path data.

3. The method according to claim 1, wherein said schematic path data comprises schematic paths, properties, hierarchy and/or verilog paths.

4. The method according to claim 1, wherein step (B) further comprises the sub-step of:

generating a list of layout coordinates and schematic instance paths as part of said compiling.

5. The method according to claim 1, further comprising
the step of:

generating one or more fuse reports in response to said
compiling.

6. The method according to claim 5, further comprising
the step of:

listing physical locations of one or more devices in
response to said fuse reports.

7. The method according to claim 1, further comprising
the step of:

generating a repair file in response to said compiling.

8. The method according to claim 7, further comprising
the step of:

creating a laser repair program in response to said
repair file.

9. The method according to claim 1, further comprising
the step of:

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verifying a function of a repair program with one or more simulations.

10. The method according to claim 1, further comprising the step of:

listing an output of said repair program as a list of coordinates of fuses that need to be blown for the desired repair in corresponding logical addresses.

11. The method according to claim 10, further comprising the step of:

storing said coordinates in a memory.

12. An apparatus comprising:

a first circuit configured to enumerate a plurality of fuses; and

5 a second circuit configured to compile data for each one of said plurality of fuses, wherein said data comprises (i) one or more schematic path data, (ii) one or more simulation path data and/or (iii) one or more physical location data.

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13. An apparatus comprising:

means for generating an enumeration of a plurality of

fuses, and

means for compiling data for each one of said plurality of fuses, wherein said data comprises (i) one or more schematic path data, (ii) one or more simulation path data and/or (iii) one or more physical location data.

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